As an inventor named below or on any attached continuation page, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled STACKED MASS STORAGE FLASH MEMORY PACKAGE, the specification of which (check one):

is attached hereto.□ was filed on as United St	ates application serial no and was	amended on		
	rnational application no and was a	mended under PCT Article 19 on	 '	
I hereby state that I have reviewed and und- referred to above.	erstand the contents of the above-identified spec	ification, including the claims, as amende	d by any amend	iment
I acknowledge the duty to disclose to the U claimed in this application, as "materiality" is	.S. Patent and Trademark Office all information defined in Title 37, Code of Federal Regulations	known to me to be material to the patents § 1.56.	tability of the su	ıbject matter
I hereby claim foreign priority benefits und certificate or § 365(a) of any PCT international priority and by a dept.	der Title 35, United States Code, § 119(a)-(d) of l application(s) designating at least one country	§ 365(b) of any foreign application(s) foother than the United States of America large any foreign application for patent or in	iventor's certific	cate or any
PCT international application(s) designating at which priority is claimed.	least one country other than the United States o	i America naving a thing date before tha	. Of the applicati	ion(s) on
Prior foreign/PCT application(s):		Priority Claimed		
(number)	(country)	(day/month/year filed)	Yes	No
(number)	(country)	(day/month/year filed)	Yes	No
application is not disclosed in any such prior a duty to disclose to the U.S. Patent and Traden Regulations § 1.56 which became available be		ragraph of Title 55, Officed States Code, naterial to patentability as defined in Title and the national or PCT international filing	237, Code of Fe	ederal
(application serial no.)	(filing date)	(status - pending, patented or		
(application serial no.)	(filing date)	(status - pending, patented or	r abandoned)	
I hereby claim the benefit under Title 35,	United States Code, § 119(e) of any United Stat	es provisional application(s) listed below:	:	,
(provisional application no.)	(filing date)			
I hereby appoint the following Registered therewith:	Practitioners to prosecute this application and to	transact all business in the Patent and Tr	ademark Office	connected
David V. Trask, Reg. No. 22,012 Joseph A. Walkowski, Reg. No. 28,765 Edgar R. Cataxinos, Reg. No. 39,931 Kenneth B. Ludwig, Reg. No. 42,814 Krista Weber-Powell, Reg. No. P-47,867 Bradley B. Jensen, Reg. No. 46,801 Michael L. Lynch, Reg. No. 30,871	William S. Britt, Reg. No. 20,969 James R. Duzan, Reg. No. 28,393 Kent S. Burningham, Reg. No. 30,453 Paul C. Oestreich, Reg. No. 44,983 Devin R. Jensen, Reg. No. 44,805 Jarett K. Abramson, Reg. No. P-47,376 Charles B. Brantley II, Reg. No. 38,086	Laurence B. Bond, Reg. No. 30 Allen C. Turner, Reg. No. 33,0 Brick G. Power, Reg. No. 38,5 David L. Stott, Reg. No. 43,93 Samuel E. Webb, Reg. No. 44, Katherine A. Hamer, Reg. No.	41 81 7 394	
TF P.:	nes R. Duzan, telephone no. (801) 532-1922. RASK BRITT O. BOX 2550 It Lake City, Utah 84110			
I hereby declare that all statements made	nerein of my own knowledge are true and that a with the knowledge that willful false statements States Code and that such willful false statement	and the like so made are dunishable by in	ne or unbrisoin	nent, or bour,

Residence: Boise, Idaho Citizenship: U.S.A. Post Office Address: 4101 Cassia, Boise, ID 83705

Full name of first joint inventor: Leonard E. Mess

Inventor's signature

Invention title: STACKED MASS STORAGE FLASH MEMORY PACKAGE

Inventor name(s) appearing on first declaration page: Leonard E. Mess

■ Additional original, first and joint inventor(s):

Full name of second joint inventor: Jerry M. Brooks

Inventor's signature

Residence: Caldwell, Idaho Citizenship: U.S.A.

Post Office Address: 1914 Ray Avenue, Caldwell, Idaho 83605

Full name of third joint inventor: David J. Corisis

Inventor's signature

Residence: Meridian, Idaho Citizenship: U.S.A.

Post Office Address: 961 W. Loon Street, Meridian, Idaho, 83642

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard E. Mess, Jerry M. Brooks and David J. Corisis

Serial No.:

Filed:

Title:

Not yet assigned

STACKED MASS STORAGE FLASH MEMORY PACKAGE

Linknown Examiner: Group Art Unit: Unknown

Attorney Docket No.: 4587US (00-0838)

POWER OF ATTORNEY BY ASSIGNEE AND CERTIFICATE UNDER 37 CFR § 3.73(b)

Commissioner for Patents Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., assignee of the entire right, title and interest by assignment from the inventor(s) in the above-identified application, hereby appoints the following attorneys and agents:

David V. Trask, Reg. No. 22,012 Joseph A. Walkowski, Reg. No. 28,765 Edgar R. Cataxinos, Reg. No. 39,931 Brick G. Power, Reg. No. 38,581 Devin R. Jensen, Reg. No. 44,805 Kerry D. Tweet, Reg. No. 45,959 Michael L. Lynch, Reg. No. 30,871

William S. Britt, Reg. No. 20,969 James R. Duzan, Reg. No. 28,393 Kent S. Burningham, Reg. No. 30,453 Kenneth B. Ludwig, Reg. No. 42,814 David L. Stott, Reg. No. 43,937 Bradley B. Jensen, Reg. No. P-46,801 Charles B. Brantley II, Reg. No. 38,086 Laurence B. Bond, Reg. No. 30,549 Allen C. Turner, Reg. No. 33,041 Stephen R. Christian, Reg. No. 32,687 Paul C. Oestreich, Reg. No. 44,983 Samuel E. Webb, Reg. No. 44,394 Eleanor V. Goodall, Reg. No. 35,162

as its attorneys with full power of substitution to prosecute this application and all applications claiming filing date priority therefrom and to transact all business in the U.S. Patent and Trademark Office in connection therewith.

The above-identified assignee hereby elects, pursuant to 37 C.F.R. § 3.71, to conduct the prosecution of the above-identified patent application to the exclusion of the inventor(s).

A chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee is shown:

- [] In an assignment recorded in the U.S. Patent and Trademark Office at Reel , Frame .
- [x] In an assignment filed herewith for recordation, a true copy of which is attached hereto.

The undersigned has reviewed the above-identified assignment and, to the best of his knowledge and belief, title is in the above-identified assignee.

The undersigned further avers that he is empowered to make and sign the foregoing certification on behalf of the above-identified assignee, and to take the action set forth herein on its behalf.

Please direct all communications regarding the above-identified application to:

James R. Duzan, TRASK BRITT P.O. Box 2550 Salt Lake City, UT 84110 Tele: (801) 532-1922 Fax: (801) 531-9168

Respectfully Submitted,

MICRON TECHNOLOGY, INC.

6-13-01

Michael L. Lynch, Esq Reg. No. 30,871 Chief Patent Counsel,

MICRON TECHNOLOGY, INC.

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